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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
10/796,859	03/08/2004	Jonathon C. Stiff	2059/US/2	2439	
20686	7590 02/23/2005		EXAM	EXAMINER	
DORSEY & WHITNEY, LLP			CUNNINGHAM, TERRY D		
INTELLECTUAL PROPERTY DEPARTMENT 370 SEVENTEENTH STREET			ART UNIT	PAPER NUMBER	
SUITE 4700			2816		
DENVER, CO 80202-5647			DATE MAILED: 02/23/2005		

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)		
Office Action Summary		10/796,859	STIFF ET AL.		
		Examiner	Art Unit		
		Terry D. Cunningham	2816		
The MAILING D	ATE of this communication app			address	
A SHORTENED STAT THE MAILING DATE - Extensions of time may be a after SIX (6) MONTHS from - If the period for reply specific If NO period for reply is spec - Failure to reply within the set	TUTORY PERIOD FOR REPLY OF THIS COMMUNICATION. vailable under the provisions of 37 CFR 1.13 the mailing date of this communication. dd above is less than thirty (30) days, a reply iffed above, the maximum statutory period w or extended period for reply will, by statute, fice later than three months after the mailing int. See 37 CFR 1.704(b).	36(a). In no event, however, m within the statutory minimum rill apply and will expire SIX (6) cause the application to becon	. ay a reply be timely filed of thirty (30) days will be considered tim MONTHS from the mailing date of this me ABANDONED (35 U.S.C. § 133).		
Status					
2a)☐ This action is FI 3)☐ Since this applic	ommunication(s) filed on NAL. 2b)⊠ This cation is in condition for allowar lance with the practice under E	action is non-final.	· ·	he merits is	
Disposition of Claims					
4a) Of the above 5) ☐ Claim(s) 6) ☑ Claim(s) <u>1-19</u> is. 7) ☐ Claim(s)		vn from consideration			
Application Papers					
10)⊠ The drawing(s) fi Applicant may not Replacement draw	is objected to by the Examine fled on <u>01 November 2004</u> is/al request that any objection to the wing sheet(s) including the correction is objected to by the Examine	re: a)⊠ accepted or drawing(s) be held in ab ion is required if the dra	eyance. See 37 CFR 1.85(a). wing(s) is objected to. See 37	CFR 1.121(d).	
Priority under 35 U.S.C.	§ 119				
12) Acknowledgmen a) All b) Son 1. Certified c 2. Certified c 3. Copies of applicatio	t is made of a claim for foreign	s have been received s have been received ity documents have b I (PCT Rule 17.2(a)).	in Application No een received in this Nation	al Stage	
Attachment(s)	• .				
1) Notice of References Cite 2) Notice of Draftsperson's P	d (PTO-892) latent Drawing Review (PTO-948) latement(s) (PTO-1449 or PTO/SB/08)	Paper	iew Summary (PTO-413) · No(s)/Mail Date e of Informal Patent Application (P	TO-152)	

DETAILED ACTION

Drawings

Figure 1 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). Corrected drawings in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Specification

The specification is objected to because page 10 is inconsistent in discussing which elements comprise the "positive feedback loop". Lines 3-5 recite elements for the "positive feedback loop" and then in lines 20-21 recites different elements. Appropriate correction is required.

Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claims 10-19 are rejected under 35 U.S.C. § 112, first paragraph, as based on a disclosure which is not enabling. Transistors M3, M6 and M8-M10 are deemed critical or essential to the practice of the invention but are not included in the claims. An arrangement lacking these

features is not enabled by the disclosure since it cannot be understood from the specification how the circuit will operate without such. *In re Mayhew*, 527 F.2d 1229, 188 USPQ 356 (CCPA 1976).

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The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 6-9 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 6, line 7, there is no antecedent for the "floating mirror".

Claims 7-9 are rejected for the reasons discussed above with claim 6.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4 and 6 are rejected under 35 U.S.C. 102(b) as being anticipated by Hosono et al. (USPAP 2001/0022527).

With respect to claims 1-4, the reference to Hosono et al. discloses, in Fig. 1, a circuit comprising: "a positive feedback loop (4a) coupled with a floating current mirror (Q51 and Q52)"; and "a negative feedback loop (Q54 and Q54), all connected and operating similarly as recited by Applicant...

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With respect to claim 6 is, clearly the above circuit to Hosono et al. will provide the recited method.

Claims 10-19 are rejected under 35 U.S.C. 102(b) as being anticipated by Applicant's prior art Fig. 1. Applicant's prior art Fig. 1 discloses "a current mirror including a first transistor (M5) and a second transistor (M4)"; "at least one resistor (RL) defining a voltage node (nbias)"; "a pull-down transistor (M1)"; and "an output transistor (M2)", all connected and operating similarly as recited by Applicant.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 5 and 7-9 are rejected under 35 U.S.C. 103(a) as being unpatentable over Hosono et al. (USPAP 2001/0022527). In the above discussed circuit of Hosono et al., such discloses bipolar transistors instead of MOSFET transistors. However, it is notoriously well known that bipolar transistors and MOSFET transistors are art recognized equivalents. Additionally, MOSFETs have the notoriously well-known advantage of reduced power consumption. Therefore, it would have been obvious for one skilled in the art to use MOSFET transistors in place of the bipolar transistors due to well-known equivalence and to obtain the expected result of reduced power consumption.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Terry Cunningham whose telephone number is 571-272-1742. The examiner can normally be reached on Monday-Thursday from 7:30 to 6:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy P. Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is (703) 872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TC February 17, 2005

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